



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

In re the Application of:

Chuang et al.

Atty Docket: **TI-36215**

Serial No.: **10/644,536**

Art Unit: **2811**

Filed: **08/20/2003**

Examiner: **TBD**

Title: **DIODE HAVING A DOUBLE IMPLANTED GUARD RING**

LETTER TO OFFICIAL DRAFTSPERSON

Mail Stop Missing Parts
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING UNDER 37 CFR 1.8(a)

I hereby certify that this document is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on:

10-14-03

Jackie McBride
Jackie McBride

Dear Sir:

In response to the Notice to File Corrected Application Papers dated September 16, 2003, please find enclosed six (6) sheets of corrected formal drawings in the application referenced above along with a return copy of said Notice.

Texas Instruments Incorporated
P. O. Box 655474 MS 3999
Dallas, TX 75265
(972) 917-5355

Respectfully submitted,

Yingsheng Tung
Yingsheng Tung
Attorney for Applicant(s)
Reg. No. 52,305